

Remarks

In the non-final Office Action dated May 29, 2009, the following new grounds of rejection are presented: claims 7 and 17 stand rejected under U.S.C. § 112; claims 1, 4-6, 9-11, 14-16 and 19-20 stand rejected under U.S.C. § 102(e) over Green (U.S. Patent Pub. 2002/0168175); claims 2-3 and 12-13 stand rejected under U.S.C. § 103(a) over the '175 reference; claims 7-8 and 17-18 stand rejected under U.S.C. § 103(a) over the '175 reference in view of Logan (U.S. Patent Pub. 2004/0255330). Applicant traverses all of the rejections and, unless explicitly stated by the Applicant, does not acquiesce to any objection, rejection or averment made in the Office Action.

Applicant respectfully traverses the § 102(e) and § 103(a) rejections because the '175 reference does not correspond to aspects of the claimed invention directed to separating I-frames from non-I-frames to generate a block of multiple I-frames and caching the block of separated I-frames without the non-I-frames. The '175 reference instead teaches that the selected I-frames I_1 and I_2 (*i.e.*, the asserted block of separated I-frames) are stored in buffers along with P-frames and B-frames. *See, e.g.*, Figure 5 and Paragraph 0083. Thus, the '175 reference does not cache the block of separated I-frames without the non-I-frames, as in the claimed invention. Applicant submits that the addition of the '330 reference does not address the deficiencies of the '175 reference. Accordingly, the § 102(e) and § 103(a) rejections are improper and must be withdrawn.

Moreover, the '175 reference also does not correspond to aspects of the claimed invention directed to the block of separated I-frames including multiple I-frames from both before and after a current playback position (*see, e.g.*, claims 2 and 12). The '175 reference instead teaches that at most two I-frames (*e.g.*, I_1 and I_2) are stored in the buffers at the same time, along with selected P-frames and B-frames. *See, e.g.*, Figure 5 and Paragraph 0083. In addition, the '175 reference further does not correspond to aspects of the claimed invention directed to the separating of the I-frames from the non-I-frames and the caching of the block of I-frames taking place during the standard play mode (*see, e.g.*, claims 3 and 13). The '175 reference instead teaches that the buffering of the selected I-frames (*e.g.*, I_1 - I_4 or I_1 and I_2) occurs in response to a command to fast-forward or reverse a stored MPEG stream. *See, e.g.*, Paragraph 0064. Thus, the

separating and buffering of the selected I-frames in the '175 reference is not done during standard playback, as in the claimed invention.

Applicant respectfully traverses the § 112 rejection of claims 7 and 17 because these claims do particularly point out and distinctly claim the subject matter which Applicant regards as the invention. With regard to claim 7, this claim does not recite "a second memory." As such, no basis for the rejection of claim 7 has been presented. Applicant notes that claim 17 has been amended to remove the word second. Thus, the § 112 rejection of claims 7 and 17 is improper and Applicant requests that it be withdrawn.

In view of the remarks above, Applicant believes that each of the rejections/objections has been overcome and the application is in condition for allowance. Should there be any remaining issues that could be readily addressed over the telephone, the Examiner is asked to contact the agent overseeing the application file, Aaron Waxler, of NXP Corporation at (408) 474-9063.

Please direct all correspondence to:

Corporate Patent Counsel
NXP Intellectual Property & Standards
1109 McKay Drive; Mail Stop SJ41
San Jose, CA 95131
CUSTOMER NO. 65913

By: 

Name: Robert J. Crawford

Reg. No.: 32,122
(NXPS.506PA)